

Panasonic

Video Signal Interface LSI

MN673793

Overview

This system LSI, MN673793 is for converting the analog video signal(NTSC) to the digital.(ITU-R Rec.656).

This LSI realizes 3 dimension Y/C separation, TBC and frame synchronizing by the single chip. So it is suitable for digital Recorders like DVD Recorders.

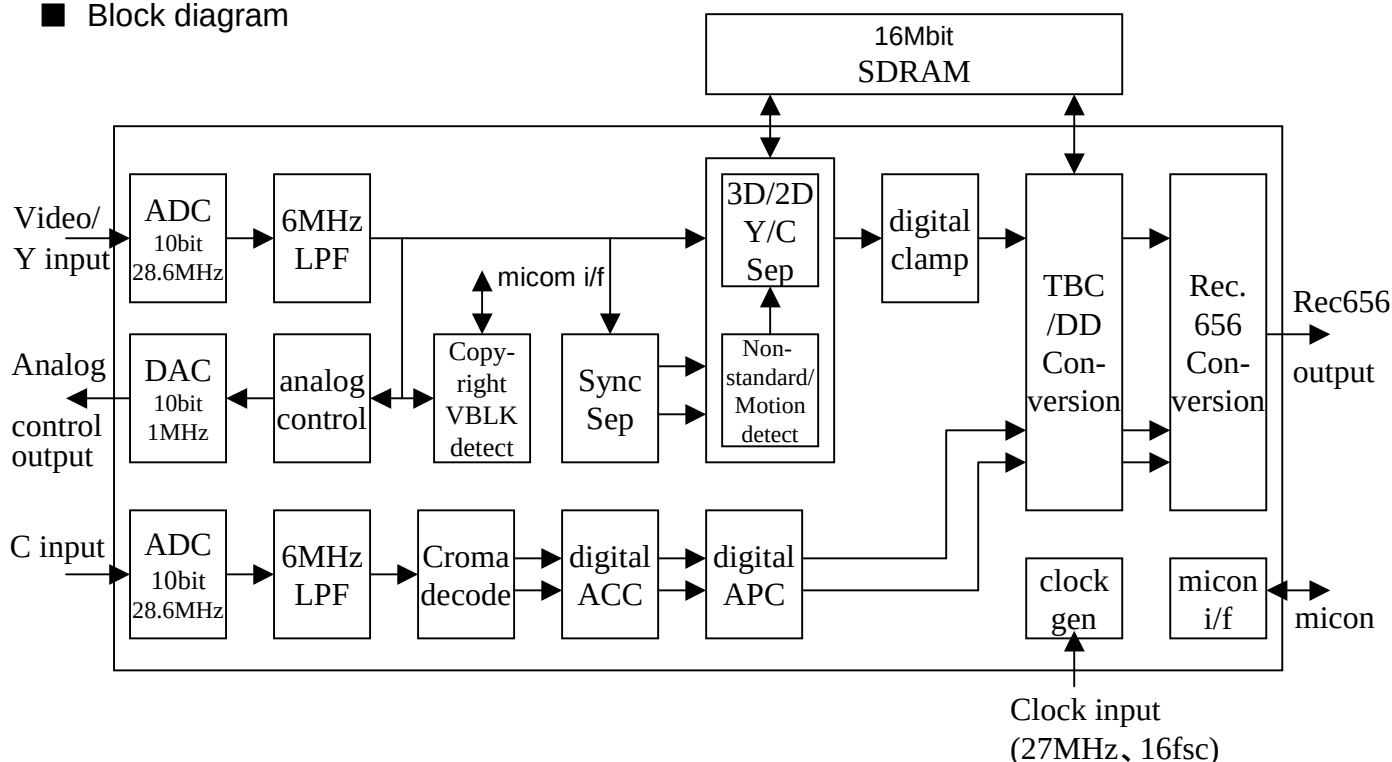
Feature

- Converting the analog video signal(NTSC) to the digital video signal(ITU-R Rec.656).
- Motion adaptive 3 dimensional Y/C separation.
- TBC(Time Base Corrector) block adopts velocity error correction
- This enables to convert even the non-standard signal like VTR playback signal to the standard digital video signal (ITU-R Rec.656).
- With This LSI and one 16 Mbit SDRAM we can execute 3 dimensional Y/C separation and frame synchronizing.

Applications

DVD Recorder, digital VIDEO Recorder

Block diagram



The products and specification are subject to change without any notice. Please ask for the latest Product Standards to guarantee the satisfaction of your product requirements.

Semiconductor Company, Matsushita Electronics Corporation

■ LSI Specification

input for Analog Video (NTSC)	Composite Video Signal (10 bit 28.6 MHz)
Analog Control	Component SVideo signal(10 bit 28.6 MHz)
Output for Digital Video	AGC(Auto Gain Control) , ACC(Auto Color Control) , Clamp Control、APC(Auto Phase Control)
Signal Processing	ITU-R Rec.656 (4:2:2 8 bit 27 MHz)
Copyright (VBLK) Detection	Motion Adaptive 3 Dimensional Y/C Separation TBC(Velocity Error Correction) Digital-Digital Conversion (14.3 MHz -> 13.5 MHz) Frame Synchronizing (non-standard video signal -> standard video signal)
Clock Frequency	Macro Vision Detection VBID Detection Closed Caption Detection
Supply Voltage	input:27 MHz , 16fsc (57.3 MHz)
Package	3.3 V $\pm$ 0.3 V
	208pinQFP (28 mm $\square$,0.5 mm pitch)

■ System Configuration

