

Owner: John
User: John
Operating System: Windows 7 Ultimate Professional 6.01.7601 Service Pack 1
Report Date: Wednesday 20 May 2015 at 14:55

<<< Processor >>>

> Processor : Intel Pentium Dual-Core E6500

>> General Information

Type : Intel Pentium Dual-Core
Internal Specification : Pentium(R) Dual-Core CPU E6500 @ 2.93GHz
Model Number : E6500
Codename : Wolfdale
Revision : R0
Technology : 0.045µ
CPU ID : 6.7.A
CPU IDEX : 6.17.A
Microcode : MU067A07
Number of Physical Processors : 1

>> Instructions

IA-64 Technology : No
Intel64 (EM64T) : Yes
AVX : No
FPU128 : No
SSE5 : No
SSE4a : No
SSE4.2 : No
SSE4.1 : No
S-SSE3 : Yes
SSE3 : Yes
SSE2 : Yes
SSE : Yes
AES : No
Extended 3DNow! Technology : No
3DNow! Technology : No
3DNow! Prefetch : No
3DNow! Pro Technology : Yes
AMD MMX Technology : No
MMX Technology : Yes
Cyrix MMX Technology : No
FMA : No
OSXSAVE : Yes
MOVBE : No
PCLMULQDQ : No
CLF - Cache Line Flush : Yes
CX8 - CMPXCHG8B : Yes
CX16 - CMPXCHG16B : Yes
CMOV - Conditionnal Move Inst. : Yes
MON - Monitor/Mwait : Yes
POPCNT : No
RDTSCP : No
SEP - Fast System Call : Yes

>> Miscellaneous

XD - No-execute Page : Yes
VT-x - Virtualization : Yes
TXT - Trusted Execution Technology : No
SVM - Secure Virtual Machine : No
FPU - Co-processor Built-in : Yes
FXSR - Fast Float Save & Restore : Yes
xTPR - Send Task Priority : Yes
DAZ - Denormals Are Zero : Yes
FXSR : No
LAHFSAHF : Yes
CMPLEGACY : No
ALTMOVC8 : No
ExtApicSpace : No
3DNow! Technology : No
PBE - Pend. Brk. EN. : No
LAHF - LAHF/SAHF Inst. : No
ABM : No
MASSE - Misaligned SSE : No
OSVW - OS Visible Workaround : No
IBS : No
P1GB - 1GB Page Size : No
SKINIT, STGI, DEV : No
WDT - Watchdog Timer : No

>> Features
x2APIC : No
APIC - Local APIC Built-in : Yes
VME - Virtual Mode Ext. : Yes
DE - Debugging Extension : Yes
PSE - Page Size Extension : Yes
TSC - Time Stamp Counter : Yes
MSR - Model Specific Registers : Yes
PAE - Physical Address Extension : Yes
MCE - Machine Check Exception : Yes
MTRR - Memory Type Range Reg. : Yes
PGE - Page Global Enable : Yes
MCA - Machine Check Architecture : Yes
PAT - Page Attribute Table : Yes
PSE36 - 36-bit Page Size Extension : Yes
PSN - Unique Serial Number : No
DS - Debug Trace & EMON Store : Yes
SS - Self Snoop : Yes
ACPI - Software Clock Control : Yes
TM - Thermal Monitor : Yes
TM2 - Thermal Monitor 2 : Yes
EST - Enhanced SpeedStep Technology : Yes
HTT - Hyper-Threading : Yes
SBF - Signal Break on FERR : Yes
DSCPL - CPL qualified Debug Store : Yes
CID - Context ID : No
LT - LaGrande Technology : No
PDCM : Yes
DCA - Direct Cache Access : No
EPS - Enhanced PowerSaver : No

>> Features Multi-Core
Physical Processor #1 (Core #1) : Apic ID 0

>> Power Status
Turbo Boost : No
Digital Thermometer : Yes
Operating Point Protection : No
Dynamic FSB : Yes
Dynamic Acceleration : No
Performance Energy Bias : No
APERF_MERF : Yes

>> Addressing Information
Physical Addressing max. : 36-bit
Linear Addressing max. : 48-bit

>> Advanced Settings
In Order Queue Depth : 8
Low Power Mode : No

>> Mainboard Upgradeability
Socket/Slot : LGA775
Upgrade interface : Unspecified
Supported Speed : 3800 MHz (or more)
Supported Voltage : 1.3V

> Frequency : 2933 MHz

>> General Information
Real Frequency : 1601.81 MHz
Multiplier : 6x
Low/High Multiplier : 6x / 11x
Multiplier Locked : Yes
Marketing Frequency : 2933.37 MHz

>> Front Side Bus Information
Bus Speed : 267 MHz
FSB Frequency : 1067.9 MHz (QDR)
Turbo Mode : No

>> Initial Frequencies
Frequency : 2933 MHz
Bus Speed : 266.67 MHz (QDR)
Multiplier : 11x

>> Frequency Control
Core #1 : 1601.74 MHz

Core #2 : 1599.94 MHz

>> Control Clock Frequency
Enhanced SpeedStep (EIST) : Yes - Enabled
C1E - Enhanced Halt Mode : No
Dynamic FSB : Yes - Disabled
Dynamic Acceleration (IDA) : No
Turbo Mode : No

>> Thermal Information
Thermal Design Power : 65 W
Core Power : 35.46 W (estimated)
Thermal control TM1 : Yes - Enabled
Thermal control TM2 : Yes - Enabled
Thermal Control Circuit : Yes
Digital Thermal Sensor : Yes
Clock Modulation (ODCM) Enabled : No

> Number of Core : 2

>> General Information
Number of Threads : 2 (max.2)
Number of Core : 2 (max. 2)
Number of Thread per Core : 1 (max. 1)

> Support : Socket 775 LGA

> Cache L1 : 2 x 64 KB

>> General Information
Write Mode : Write-Back
Place : On Chip
Correct : Parity

>> Cache Information
Data Cache : 2 x 32 KB (8-way, 64 bytes line size)
Code Cache : 2 x 32 KB (8-way, 64 bytes line size)
Number of Threads : 1

>> Cache Parameters
Type : Data Cache
Ways of associativity : 8
Fully Associative : No
Self Initializing : Yes
System Coherency Line Size : 64
Physical Line partitions : 1
Number of threads sharing : 1
Number of processor cores : 2
Number of Sets : 64

>> Cache Prefetcher Optimization
Adjacent Cache Line : Yes (128-bit)
DPL L2 Cache : Yes
DCU L1 Cache : Yes
IP L1 Cache : Yes

> Cache L2 : 2048 KB

>> General Information
Write Mode : Write-Back
Place : On Chip
Multiplier : 1/1x (1599.9 MHz)
Cache Latency : 3 clocks
Correct : Single-bit ECC

>> Cache Information
Associativity : 8-way
Line Size : 64 bytes
Number of Threads : 2
Bus : 256-bit
Prefetch Logic : Yes

>> Cache Parameters
Type : Unified Cache
Ways of associativity : 8
Fully Associative : No

Self Initializing : Yes
System Coherency Line Size : 64
Physical Line partitions : 1
Number of threads sharing : 2
Number of processor cores : 2
Number of Sets : 4096

>> Cache Prefetcher Optimization
Adjacent Cache Line : Yes (128-bit)
DPL L2 Cache : Yes
DCU L1 Cache : Yes
IP L1 Cache : Yes

> Voltage VID : 1.300 V

>> General Information CPU
Voltage : 1.288 V
MaxVID : 1.288 V
MinVID : 1.100 V

> FPU Coprocessor : Present

>> General Information
Integrated : Yes
Model : Compatible Intel

> Core 1 Activity : 0%

> Core 2 Activity : 1%

***** End of report *****